

WHAT IS CLAIMED IS:

1. A selector comprising:

a plurality of address terminals;

a plurality of selector-output terminals; and

5 a plurality of group identification terminals which receive group identification signals that contain information about number of divisions into which said selector-output terminals are to be divided into groups,

wherein, of the groups divided based on the number of  
10 divisions specified by the group identification signals, the group specified with the address signals is selected based on the group identification signals that are input from said group identification terminals and the address signals that are input from said address terminals, and the selector-output signal  
15 that is to be output from the selector-output terminal within the selected group is made effective.

2. The selector according to claim 1, wherein a particular selector-output terminal of said plurality of selector-output  
20 terminals is specified with addresses obtained by,

dividing said plurality of selector-output terminals into a plurality of groups in a first division unit,

assigning an address that identifies a corresponding group to each of the divided groups,

25 further dividing each of the groups divided in the first

division unit into a plurality of groups in a second division unit,

assigning an address that identifies a corresponding group to each of the groups divided in the second division unit,

5 repeating division of groups and assignment of addresses to the divided groups for a specified number of times,

wherein the group identification signal is a signal that specifies the number of divisions, and

10 wherein the address signal is a signal that specifies one of the addresses that are assigned to the divided groups.

3. The selector according to claim 1, wherein a particular selector-output terminal of said plurality of selector-output terminals is specified with addresses obtained by,

15 dividing said plurality of selector-output terminals into groups each of which includes one quarter of the terminals,

assigning 2-bit addresses of "00", "01", "10", and "11" to the divided groups,

20 further dividing each of the divided groups into groups each of which includes one quarter of the group,

further assigning 2-bit addresses of "00", "01", "10", and "11" to each of the divided groups, and

repeating division of groups and assignment of addresses to the divided groups for a specified number of times,

25 wherein the group identification signal is a signal that

specifies the number of divisions, and

wherein the address signal is a signal that specifies one of the addresses that are assigned to the divided groups.

5 4. A selector comprising:

a plurality of address terminals;

a plurality of selector-output terminals;

a plurality of group identification terminals which receive group identification signals that contain information  
10 about number of divisions into which said selector-output terminals are to be divided into groups; and

a selection output terminal which outputs a selection signal that identifies the selector-output signals that are output from said selector-output terminal,

15 wherein, of the groups divided based on the number of divisions specified by the group identification signals, the group specified with the address signals is selected based on the group identification signals that are input from said group identification terminals and the address signals that are input  
20 from said address terminals, and the selector-output signal that is to be output from the selector-output terminal within the selected group is made effective, and a selection signal that identifies the selector-output signals that are output in response to input of the address signals is output from said  
25 selection output terminal.

5. The selector according to claim 4, wherein a particular selector-output terminal of said plurality of selector-output terminals is specified with addresses obtained by,

5 dividing said plurality of selector-output terminals into a plurality of groups in a first division unit,

assigning an address that identifies a corresponding group to each of the divided groups,

10 further dividing each of the groups divided in the first division unit into a plurality of groups in a second division unit,

assigning an address that identifies a corresponding group to each of the groups divided in the second division unit,

15 repeating division of groups and assignment of addresses to the divided groups for a specified number of times,

wherein the group identification signal is a signal that specifies the number of divisions, and

wherein the address signal is a signal that specifies one of the addresses that are assigned to the divided groups.

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6. The selector according to claim 4, wherein a particular selector-output terminal of said plurality of selector-output terminals is specified with addresses obtained by,

25 dividing said plurality of selector-output terminals into groups each of which includes one quarter of the terminals,

assigning 2-bit addresses of "00", "01", "10", and "11"  
to the divided groups,

further dividing each of the divided groups into groups  
each of which includes one quarter of the group,

5 further assigning 2-bit addresses of "00", "01", "10",  
and "11" to each of the divided groups, and

repeating division of groups and assignment of addresses  
to the divided groups for a specified number of times,

wherein the group identification signal is a signal that  
10 specifies the number of divisions, and

wherein the address signal is a signal that specifies one  
of the addresses that are assigned to the divided groups.